

Notice of References Cited

Application/Control No.

09/699,466

Applicant(s)/Patent Under
Reexamination
YAMAZAKI ET AL.

Examiner

Asok K. Sarkar

Art Unit

2829

Page 1 of 1

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NON-PATENT DOCUMENTS

*		Include as applicable: Author, Title Date, Publisher, Edition or Volume, Pertinent Pages)
	U	Wolf, "Silicon Processing for the VLSI Era", Vol. 2, Chapter 4, last paragraph of page 274(1990).
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*A copy of this reference is not being furnished with this Office action. (See MPEP § 707.05(a).)
Dates in MM-YYYY format are publication dates. Classifications may be US or foreign.